

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:	§	Patent No.:	7,627,792
Mario Di Ronza et al.	§	Issued:	December 1, 2009
	§		
Serial No.: 10/777,025	§	Group Art Unit:	2117
	§		
Filed: February 11, 2004	§	Examiner:	Cynthia H. Britt
For:			
MEMORY BUILT-IN SELF REPAIR (MBISR) CIRCUITS/DEVICES AND METHOD FOR REPAIRING A MEMORY COMPRISING A MEMORY BUILT-IN SELF REPAIR (MBISR) STRUCTURE			

Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

REQUEST FOR CERTIFICATE OF CORRECTION

Attached is a Certificate of Correction for correcting errors in the specification section of the printed patent.

Applicant submits that the errors mentioned above were not by the applicant, but were made during the printing of the patent.

Respectfully submitted, and
S-signed pursuant to 37 CFR 1.4,

/Joseph M. JONG, Reg. #42698/

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**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

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APPLICATION NO. : 10/777,025
ISSUE DATE : December 1, 2009
INVENTOR(S) : Mario Di Ronza et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 5, line 12, delete "Col address registers)" and insert --Col address register (j)--.

In column 5, line 17, delete "Col address registers)" and insert --Col address register (j)--.

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